

High frequency inverter for buildings





Overview

What is a high frequency variable load inverter architecture?

This thesis presents a high frequency variable load inverter architecture along with a physical prototype and efficiency optimizing controller. The inverter architecture consists of two constituent inverters, one connected directly through the load and the other connected through an immittance converter, which acts as a lossless power combiner.

What is a high frequency inverter?

I. INTRODUCTION Many applications – ranging from industrial plasma generation to wireless power transfer – require inverters (or power amplifiers) that can deliver power at high frequency (HF, 3-30 MHz).

Can inverters provide efficient delivery of high-frequency power into variable load impedances?

VI. CONCLUSION This paper introduces an inverter architecture and associated control approach for providing efficient delivery of high-frequency power into variable load impedances while maintaining resistive/inductive loading of the constituent inverters for ZVS soft switching.

Is a new inverter architecture suitable for varying load impedances?

Abstract: This paper presents a new inverter architecture suitable for driving widely varying load impedances at high frequency (HF, 3-30 MHz) and above. We present the underlying theory and design considerations for the proposed architecture along with a physical prototype and efficiency optimizing controller.



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